

REMARKS

Claims 21-31 are canceled, leaving claims 1-20 pending in the application. In response to the Examiner's request for restriction of the claims in the application, Applicant elects to prosecute the claims of Group II. (claims 1-20). Applicant requests substantive examination of the pending claims.

Respectfully submitted,

Dated: 11/6/01

By: 

David G. Laursen, Ph.D.  
Reg. No. 38,533

BEST AVAILABLE COPY



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/651,422  
Filing Date ..... August 30, 2000  
Inventor ..... Jeffrey W. Honeycutt et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2815  
Examiner ..... A. Wilson  
Attorney's Docket No. .... MI22-1332  
Title: Transistor Structures Methods of Forming Transistor Structures, and  
Methods of Forming Insulation and Conductive Structures

**VERSION WITH MARKINGS TO SHOW CHANGES MADE  
ACCOMPANYING RESPONSE TO OCTOBER 16, 2001 OFFICE ACTION**

**In the Claims**

Cancel claims 21-31.

BEST AVAILABLE COPY

END OF DOCUMENT